

FIG. 1

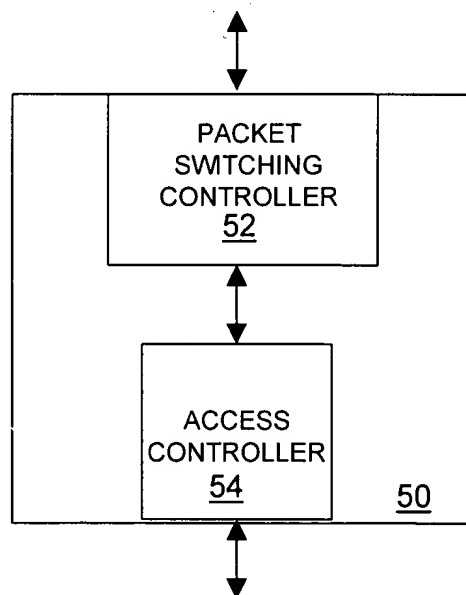


FIG. 2

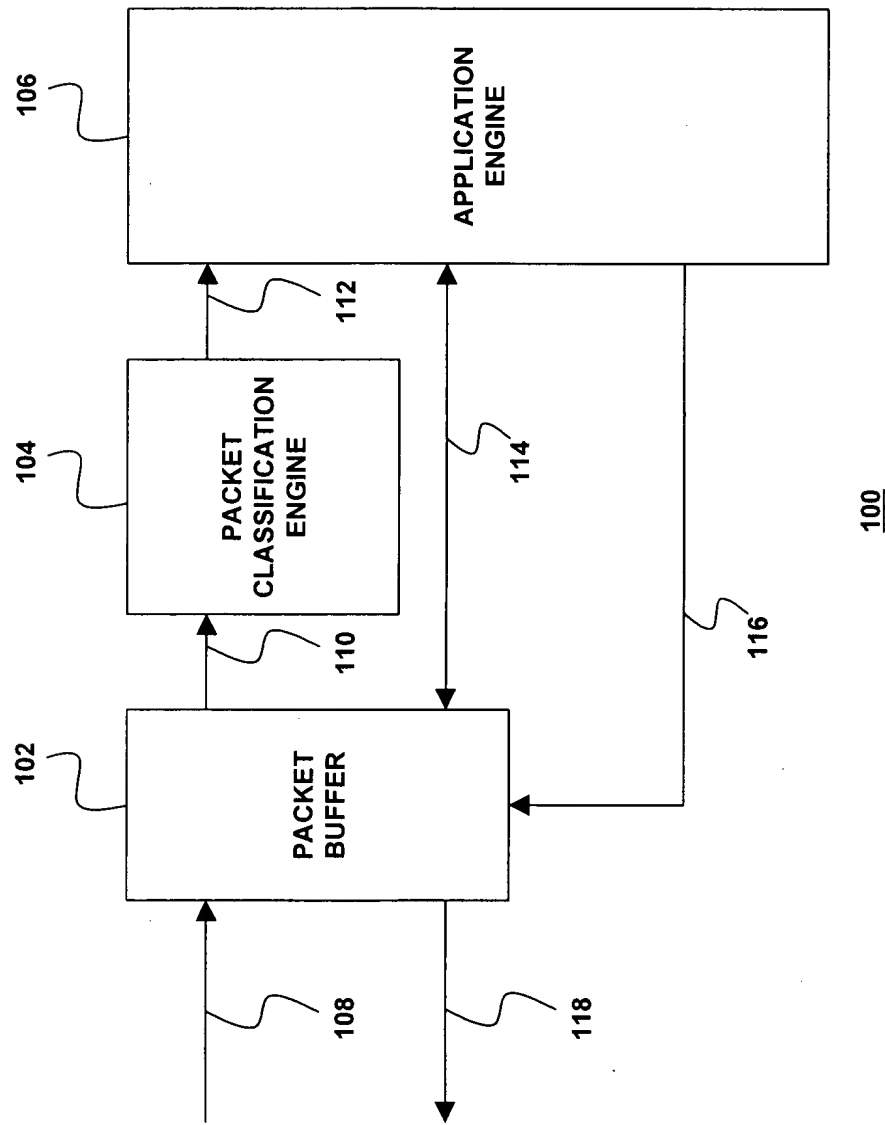
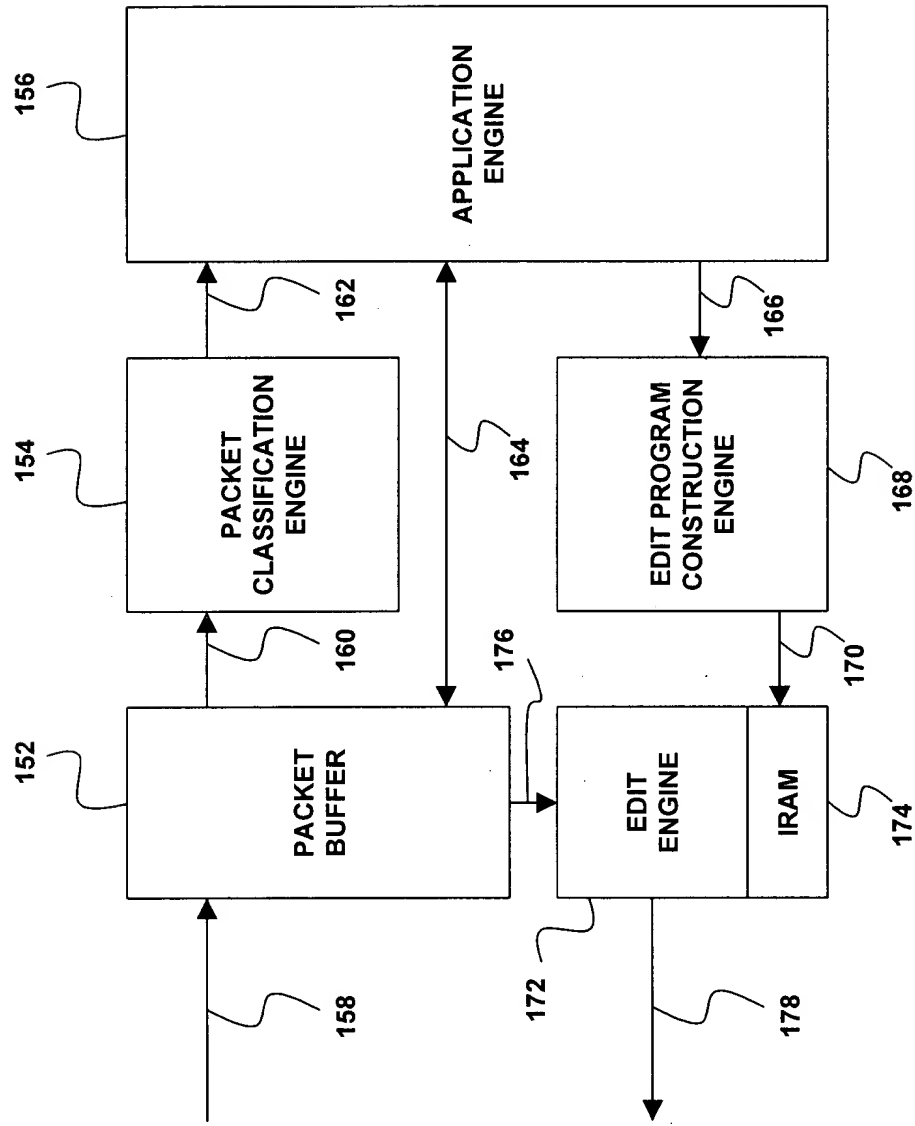


FIG. 3

FIG. 4 is a block diagram of a system 150 for processing data packets. The system 150 includes a packet buffer 152, a packet classification engine 154, an application engine 156, an edit engine 172, an IRAM 174, and an edit program construction engine 168. The packet buffer 152 receives data from an input 158 and outputs data to the packet classification engine 154. The packet classification engine 154 outputs data to the application engine 156. The application engine 156 outputs data to the edit engine 172. The edit engine 172 outputs data to the IRAM 174. The IRAM 174 outputs data to the edit program construction engine 168. The edit program construction engine 168 outputs data to the application engine 156. The application engine 156 outputs data to the output 178.



150

FIG. 4

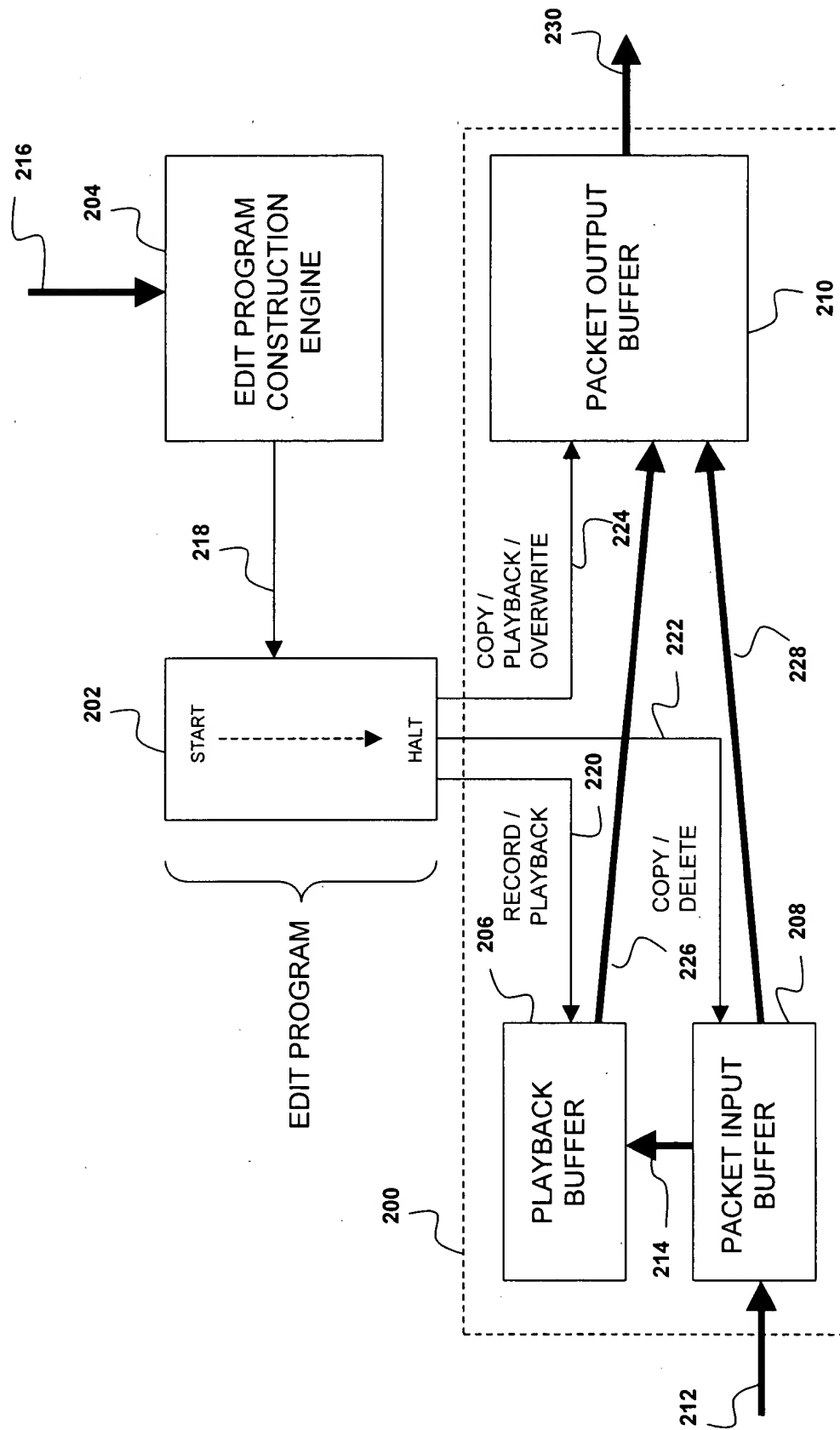


FIG. 5

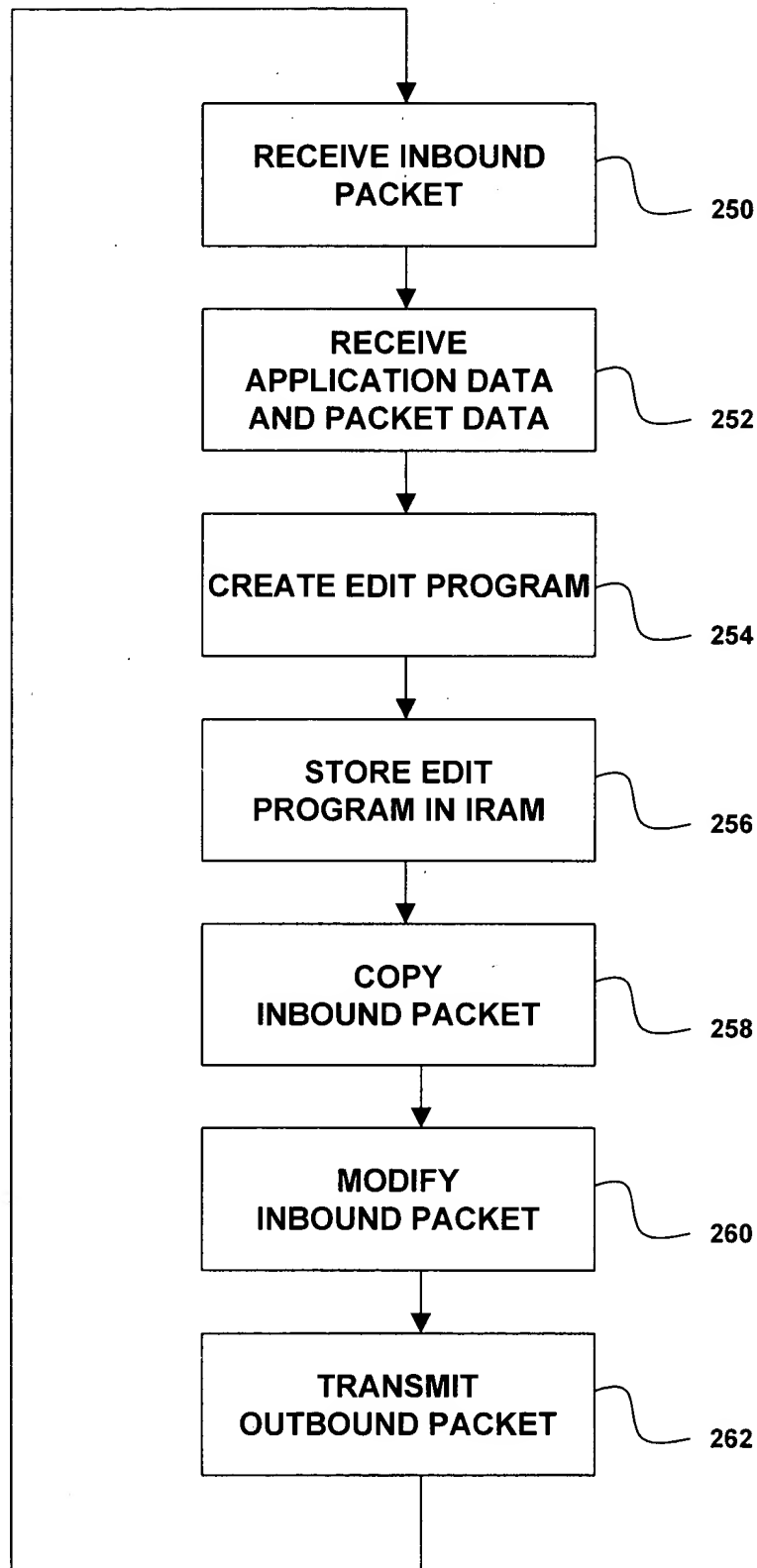


FIG. 6